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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
09/199,776	11/25/98	LEE S	P55394

TM02/0425

ROBERT E BUSHNELL
1522 "K" STREET NW
SUITE 300
WASHINGTON DC 20005-1202

EXAMINER	
ZAMANI, A	
ART UNIT	**PAPER NUMBER**
2674	9

DATE MAILED: 04/25/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.	Applicant(s)
09/199,776	Sang Hae-LEE
Examiner	**Group Art Unit**
Ali Zamani	2674

☒ Responsive to communication(s) filed on *Feb 7, 2001*

☐ This action is **FINAL.**

☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claim

☒ Claim(s) *2, 6-9, and 21-50* is/are pending in the applicat

Of the above, claim(s) _____ is/are withdrawn from consideration

☐ Claim(s) _____ is/are allowed.

☒ Claim(s) *2, 6-9, and 21-50* is/are rejected.

☐ Claim(s) _____ is/are objected to.

☐ Claims _____ are subject to restriction or election requirement.

Application Papers

☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on _____ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some* ☒ None of the CERTIFIED copies of the priority documents have been

☐ received.

☐ received in Application No. (Series Code/Serial Number) _____.

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

☒ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____

☐ Interview Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

— SEE OFFICE ACTION ON THE FOLLOWING PAGES —

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 2,6-9 and 21-50 are rejected under 35 U.S.C. 103(a) as being unpatentable over McNally (US Pat. No. 5,608,418) in view of Miyamoto et al. (US Pat. No. 6,097,364).

3. In regard to claims 2, 6- 9 and 21-50 McNally discloses a method, comprising: the computer graphics system (10) comprises a processor (20), a graphic subsystem (22), a display (26), and a CRT display (28). McNally teaches that while power is being supplied to the processor (20) communicates with the graphics subsystem (22) over a system bus (24). The processor (20) executes computer graphics application programs. The computer graphics application program generate graphics data that define graphical elements for display. The processor (20) processing data including the varying visual information (Fig. 1, col. 3, lines 1-15). McNally also disclose the graphics subsystem (22) comprises a rendering processor (40), a RAMDAC (42), a color buffer circuit (44), a power circuit (54) and a video random access memory (46), a bus connector (60) couples the system bus (24) for communication to the rendering processor (40), the color buffer circuit (44), a programmable clock generator circuit

(50) and (ID-PROM) 52 over a data bus (70). The processor (20) reads the sense data bits from the internal register of the color buffer circuit (44) over the data bus (70) if the CRT sense signals (84) indicate that the CRT displaying (28) is not coupled to the CRT interface lines (32). The processor (20) uses the sense data bits to determine a configuration for graphics subsystem (22) to drive the flat panel display (26). The processor (20) accordingly programs the PCG (50), the rendering processor (40) and the RAMDAC (42) to drive the flat panel display (26) (Figs 2 and 3, cols 3 and 4). McNally substantially teaches the above claimed limitations excpt for teaching whether said " first data corresponds to second data stored in a memory unit; and when said first data doen not correspond to second data in said memory unit". However, Miyamoto et al. disclose a display control apparatus with independent information receivers include a display control device (2), including a computer and a personal computer, a display panel unit (3), a display signal reception unit (4), a color converter (5), a pseudo haftone processing unit (6), a synthesis unit (7), a copression nit (8), a partial write control unit (10). Miyamoto et al. teach that the signal from the compreeion unit (8) is also sent to the partial write control unit (10) (Fig. 1, cols 3 and 4) which reads the compressed data of one frame back from the frame memory and compares it with the compressed data sent from the comressed data sent from the comressed data line by line and the partial write control unit (10) detects a line including non-matched pixeles based on the two compressed data from the frame memory (11) so that the compressed data of thatline is preferentially outputted to the expension unit (9) (col. 6, lines 5-63). Miyamoto etal. also teach that the compression may be done not line by line by several lines at a time or field by

field and the differenc between the previous screen and the screen under consideration may be done by expanding the compressed data and comparing the expanded data pixel by pixel. It would have been obvious to one of ordinary skill in the art to combine method of McNally and the noted teaching of Miyamoto et al. to coprovide a hot-plugging method of display in which a main body of a main computer automatically recognizes a newly connected display device.

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Matsumoto et al. and Inoue are made of record to show various type of hot-plugging method

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ali Zamani whose telephone number is (703) 308-6414. The examiner can normally be reached on Monday through Friday from 8:00 a.m to 5:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe, can be reached on (703) 305-4709. The fax phone number for the organization where this application or proceeding is assigned is (703) 308-9051.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Ali Zamani

April 20, 2001

RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600